

MAR 13 2006

Attorney's Docket No.: 10559-755001  
Client's Ref. No.: P13653

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Number of pages including this page 3

Applicant : Satyendra Yadav  
Serial No. : 10/066,140  
Filed : February 1, 2002

Art Unit : 2132  
Examiner: Venkatanaray Perungavoor  
Assignee : Intel Corporation

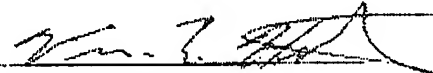
Title : Integrated Network Intrusion Detection

**Mail Stop Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is INTERVIEW SUMMARY and PROPOSED AMENDMENT, faxed this 13<sup>th</sup> day of March, 2006, to the United States Patent and Trademark Office.

Respectfully submitted,



William E. Hunter  
Reg. No. 47,671

Date: March 13, 2006

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Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Satyendra Yadav Art Unit: 2132  
Serial No.: 10/066,140 Examiner: Venkatanaray Perungavoor  
Filed : February 1, 2002 Assignee: Intel Corporation  
Title : INTEGRATED NETWORK INTRUSTION DETECTION

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INTERVIEW SUMMARY

Examiners Perungavoor and Zand are thanked for the interview, which was conducted with Applicants' representative, Mr. Hunter, on February 13, 2006 (please note that the Examiner's Interview Summary is in error in giving the interview date as February 15, 2006). During the interview, Examiner Zand indicated that independent claims 13, 26, and 31 were in condition for allowance, and that independent claims 1, 22, and 24 would likely be allowable if a feature of claim 5 were added to claims 1 and 22, and a corresponding feature of claim 25 were added to claim 24.

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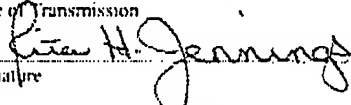
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Applicant : Satyendra Yadav  
Serial No. : 10/066,140  
Filed : 2/1/2002  
Page : 2

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